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$v_o = 10\text{ V}$ with a 1-k load, calculate the output resistance and the loop gain of the regulator. If a 500- load is connected to the regulator in place of the 1-k load, calculate the new value of v_o . Use SPICE to determine the line regulation and load regulation of the circuit.

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Paul R. Gray received the BS, MS, and PhD degrees from the University of Arizona. He joined the University of California, Berkeley in 1971 with the Department of Electrical Engineering and Computer Sciences. Gray's research interests include bipolar and MOS circuit design, electro thermal interactions in integrated circuits, device modeling, telecommunications circuits, and analog-digital interfaces on analog integrated circuits.

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